

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First

Named

Inventor: Artur E. Balasinski, et al.

Pat. No.: 6,834,262 B1

Issue Date: December 21, 2004

Title: A SCHEME FOR IMPROVING THE
SIMULATION ACCURACY OF
INTEGRATED CIRCUIT PATTERNS
BY SIMULATION OF THE MASK

TRANSMITTAL

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Commissioner for Patents

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